

Digital Transistors (Built-in Resistors)

• Equivalent Circuit

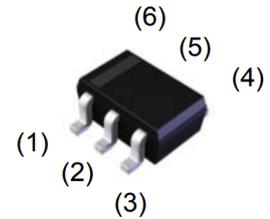
DIGITAL TRANSISTOR (NPN)

FEATURES

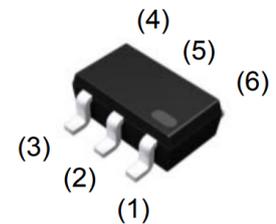
- Two DTC114T chips in a UMT or SMT package.
- Mounting possible with UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- Mounting cost and area can be cut in half.

Inner circuit

SOT23-6L



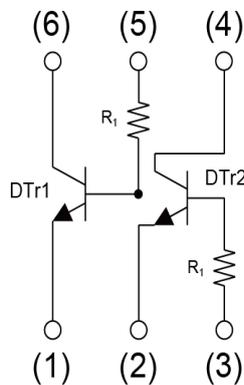
UMH8N



IMH8A

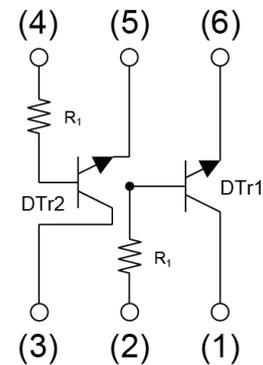
UMH8N

- (1) DTr1 Emitter
- (2) DTr2 Emitter
- (3) DTr2 Base
- (4) DTr2 Collector
- (5) DTr1 Base
- (6) DTr1 Collector



IMH8A

- (1) DTr1 Collector
- (2) DTr1 Base
- (3) DTr2 Collector
- (4) DTr2 Base
- (5) DTr2 Emitter
- (6) DTr1 Emitter



ORDERING INFORMATION

Part Number	MARKING	Package	Packing Method	Pack Quantity
UMH8N	H8	SOT-23-6L	Reel	3000pcs/Reel
IMH8A	H8	SOT-23-6L	Reel	3000pcs/Reel

MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Values	Unit
V _{CC}	Supply Voltage	50	V
V _{IN}	Input Voltage	-7~+20	V
I _O	Output Current	100	mA
P _D	Power Dissipation	150	mW
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55~+150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =50μA, I _E =0	50			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1mA, I _B =0	50			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =50μA, I _C =0	5			V
Collector cut-off current	I _{CBO}	V _{CB} =50V, I _E =0			0.5	μA
Emitter cut-off current	I _{EBO}	V _{EB} =4V, I _C =0			0.5	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =10mA, I _B =1mA			0.3	V
DC current gain	h _{FE}	V _{CE} =5V, I _C =1mA	100	300	600	
Input resistor	R ₁		7	10	13	kΩ
Transition frequency	f _T	V _{CE} =10V, I _E =-5mA, f=100MHz		250		M Hz

Typical Characteristi
