

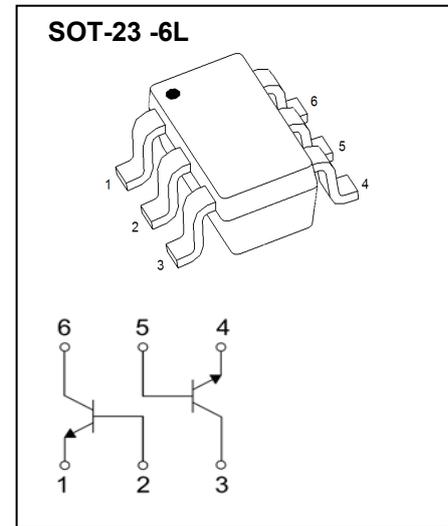
Plastic-Encapsulate Transistors

DUAL TRANSISTOR (NPN+NPN)

FEATURE

Complementary PNP Type available MMDTSS8550F

MARKING: DY1



MAXIMUM RATINGS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-base voltage	V_{CBO}	40	V
Collector-emitter voltage	V_{CEO}	25	V
Emitter-base voltage	V_{EBO}	5	V
Collector continuous current	I_C	1.5	A
Collector power dissipation	P_C	300	mW
Thermal resistance from junction to ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Operating junction and storage temperature range	T_j, T_{stg}	-55 ~ 150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 100\mu\text{A}, I_E = 0\text{A}$	40	-	-	V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 0.1\text{mA}, I_B = 0\text{A}$	25	-	-	V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = 100\mu\text{A}, I_C = 0\text{A}$	5	-	-	V
Collector-base cut-off current	I_{CBO}	$V_{CB} = 40\text{V}, I_E = 0\text{A}$	-	-	0.1	μA
Collector cut-off current	I_{CEO}	$V_{CE} = 20\text{V}, I_B = 0\text{A}$	-	-	0.1	μA
Emitter-base cut-off current	I_{EBO}	$V_{EB} = 5\text{V}, I_C = 0\text{A}$	-	-	0.1	μA
DC current gain	$h_{FE(1)}$	$V_{CE} = 1\text{V}, I_C = 100\text{mA}$	200	-	350	-
	$h_{FE(2)}$	$V_{CE} = 1\text{V}, I_C = 800\text{mA}$	40	-	-	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 800\text{mA}, I_B = 80\text{mA}$	-	-	0.5	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = 800\text{mA}, I_B = 80\text{mA}$	-	-	1.2	V
Transition frequency	f_T	$V_{CE} = 10\text{V}, I_C = 50\text{mA}, f = 30\text{MHz}$	100	-	-	MHz

Typical Characteristics

